

Electronic Acknowledgement Receipt

EFS ID:	2189503
Application Number:	10678055
International Application Number:	
Confirmation Number:	4569
Title of Invention:	Method and apparatus for reducing stress across capacitors used in integrated circuits
First Named Inventor/Applicant Name:	Ramkarthik Ganesan
Customer Number:	8791
Filer:	William W. Schaal/Susan McFarlane
Filer Authorized By:	William W. Schaal
Attorney Docket Number:	42P6963R
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		P6963R.PDF	581918 87eacf6fb18c3dd11addcd020b99ef 33396913	yes	14

Multipart Description/PDF files in .zip description			
Document Description	Start	End	
Amendment - After Non-Final Rejection	1	1	
Claims	2	10	
Applicant Arguments/Remarks Made in an Amendment	11	13	
Authorization for Extension of Time all replies	14	14	

Warnings:

Information:	Total Files Size (in bytes):	581918
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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